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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/735,668	12/20/2000	Rodolfo I. Gamboa	TI-29518	5377
23494 7	7590 07/27/2005		EXAM	INER
	TRUMENTS INCOR	FARAHANI, DANA		
P O BOX 6554	•		ADTIBUTE DARED MUNICIPALITA	
DALLAS, TX	75265		ART UNIT	PAPER NUMBER
			2891	

DATE MAILED: 07/27/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

			X	
	Application No.	Applicant(s)	Ø	
	09/735,668	GAMBOA, RODO	GAMBOA, RODOLFO I.	
Office Action Summary	Examiner	Art Unit		
	Dana Farahani	2891		
The MAILING DATE of this communication ap Period for Reply	pears on the cover sheet v	vith the correspondence ac	ddress	
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a rep - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statut Any reply received by the Office later than three months after the mailir earned patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a sly within the statutory minimum of th will apply and will expire SIX (6) MC e, cause the application to become A	reply be timely filed irty (30) days will be considered timel NTHS from the mailing date of this c		
Status				
Responsive to communication(s) filed on <u>28 A</u> This action is FINAL . 2b) ☑ This Since this application is in condition for allowed closed in accordance with the practice under	s action is non-final. ance except for formal ma		e merits is	
Disposition of Claims				
4) Claim(s) 1-22 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) Claim(s) 1-17 is/are allowed. 6) Claim(s) 18-22 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/o	awn from consideration.		·	
Application Papers				
9) The specification is objected to by the Examin 10) The drawing(s) filed on 20 December 2000 is/ Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the E	are: a) accepted or b) to accepted or b) to accepted or b) to accepted in abeyont or acception is required if the drawing the acception is required if the drawing accepted in the accepted in	ance. See 37 CFR 1.85(a). g(s) is objected to. See 37 C	FR 1.121(d).	
Priority under 35 U.S.C. § 119				
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority document application from the International Bureat * See the attached detailed Office action for a list	nts have been received. Its have been received in prity documents have bee au (PCT Rule 17.2(a)).	Application No n received in this National	l Stage	
Attachment(s)			•	
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 	Paper No	Summary (PTO-413) o(s)/Mail Date Informal Patent Application (PT	O-152)	
Paper No(s)/Mail Date	6) Other: _	·		

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DETAILED ACTION

Drawings

1. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference sign(s) mentioned in the description: element 31.

- 2. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they do not include the following reference sign(s) mentioned in the description: element 70.
- 3. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

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The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

5. Claim 21 is rejected under 35 U.S.C. 102(e) as being anticipated by Saito (US Patent 6,304,073), newly cited.

Saito discloses in figures 7-9, a method of testing a semiconductor device, shown as IC, having a plurality of pins (I/O pins), the method comprising:

providing a test socket 50 having a body for receiving a semiconductor device, the test socket including a plurality of guideposts 52 integrally formed within the body and a plurality of pogo-pins 51 adjacent to one another for directly contacting the plurality of pins;

aligning the semiconductor device within the body utilizing the guide posts (see figure 7);

applying a pressure which brings the plurality of semiconductor device pins into contact with the plurality of pogo-pins (see figure 8); and testing the semiconductor device.

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Claim Rejections - 35 USC § 103

6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 7. Claim 22 is rejected under 35 U.S.C. 103(a) as being unpatentable over Saito as applied to claim 21 above, and further in view of An et al., hereinafter An (US Patent 6,489,790), newly cited.

Saito discloses the limitations in the claim, as discussed above, but does not disclose the pogo-pins have internal springs.

An discloses in figure 6, springs are located inside of pogo-pins 118, in a test socket for a chip, as can be seen in the figure. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to implement springs inside the pogo-pins of the structure of Saito in order to alleviate excessive force or pressure to the chip contacts and/or the pogo-pins themselves, when testing a chip in the structure.

8. Claim 18-20 are rejected under 35 U.S.C. 102(b) as anticipated by or, in the alternative, under 35 U.S.C. 103(a) as obvious over Katz (US Patent 4,438,397), in view of Saito.

Regarding claims 18 and 19, Katz discloses in figure 1, a pin structure for testing a chip package or a Printed Circuit Board. This pin has a crowned shaped end, which upon contact to the pins of a chip package would invariably leave a plurality of contact marks with a pitch.

Although, Katz does not discloses the novel pin structure contacts the pins of a chip package, as

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discussed above with respect to the Saito reference, the test pins in a test structure would contact the pins of a semiconductor chip package in a testing process of said chip package.

Regarding claim 20, although Katz does not explicitly state the pitch is less than 0.14 mm, it states at column 2, line 4, that the diameter of the spring portion of the pogo pins is 5/1000 of an inch, which means the test pin is very small. Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to make the pitch of the crown portion of the pin less than a particular value (in this case 0.14 mm) to make it suitable for the small pins of a chip package. Note that the specification contains no disclosure of either the critical nature of the claimed dimensions of any unexpected results arising therefrom. Where patentability is aid to be based upon particular chosen dimensions or upon another variable recited in a claim, the Applicant must show that the chosen dimensions are critical. *In re Woodruff*, 919 F.2d 1575, 1578, 16 USPQ2d 1934, 1936 (Fed. Cir. 1990).

Allowable Subject Matter

- 9. Claims 1-17 are allowed.
- 10. The following is an examiner's statement of reasons for allowance:

The reason for indication of allowability of claims 1-17 is the inclusion therein of the limitation that of the plurality of pogo-pins, with the specific stated physical configuration, along with the other structural limitations in the independent claim 1.

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Response to Arguments

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11. Applicant's arguments, see the response, filed on 4/28/05, with respect to the rejection of claim 18 under 35 USC § 102(b) have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, new grounds of rejection are made (see above).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dana Farahani whose telephone number is (571)272-1706. The examiner can normally be reached on M-F 9:00AM - 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bill Baumeister can be reached on (571)272-1722. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

D. Farahani

B. WILLIAM BAUMEISTER SUPERVISORY PATENT EXAMINED